



TEST DATA OF CBS1002424

(24V INPUT)

Regulated DC Power Supply
Jun. 20, 2002

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Kouichi Kinoshita Design Engineer

コーセル株式会社
COSEL CO.,LTD.

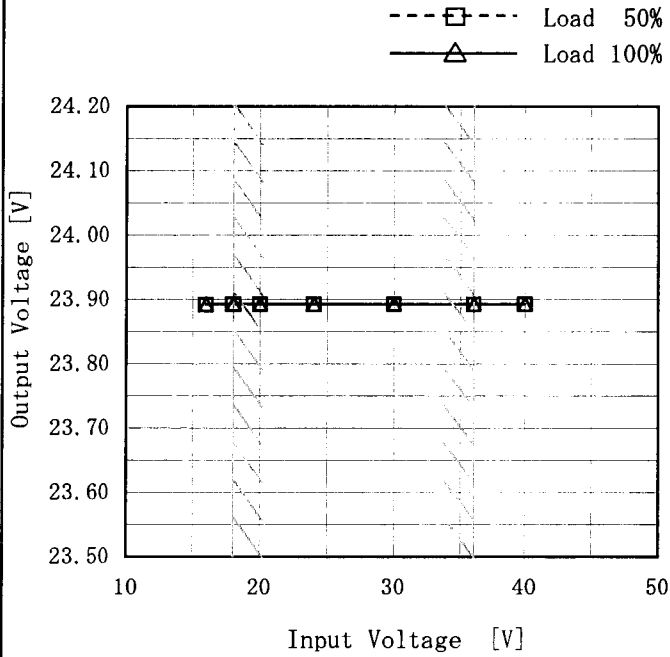
CONTENTS

1. Line Regulation	1
静的入力変動	
2. Input Current (by Input Voltage)	2
入力電流 (入力電圧特性)	
3. Input Current (by Load Current)	3
入力電流 (負荷特性)	
4. Input Power (by Load Current)	4
入力電力 (負荷特性)	
5. Efficiency (by Input Voltage)	5
効率 (入力電圧特性)	
6. Efficiency (by Load Current)	6
効率 (負荷特性)	
7. Load Regulation	7
静的負荷変動	
8. Ripple Voltage (by Load Current)	8
リップル電圧 (負荷特性)	
9. Ripple-Noise	9
リップルノイズ	
10. Overcurrent Protection	10
過電流保護	
11. Overvoltage Protection	11
過電圧保護	
12. Dynamic Load Response	12
動的負荷変動	
13. Rise and Fall Time	13
立上り、立下り時間	
14. Ambient Temperature Drift	14
周囲温度変動	
15. Minimum Input Voltage for Regulated Output Voltage	15
最低レギュレーション電圧	
16. Ripple Voltage (by Ambient Temperature)	16
リップル電圧 (周囲温度特性)	
17. Time Lapse Drift	17
経時ドリフト	
18. Output Voltage Accuracy	18
定電圧精度	
19. Condensation	19
結露特性	
20. Line Noise Tolerance	20
入力雑音耐量	
21. Figure of Testing Circuitry	21
測定回路図	

(Final Page 21)

Model	CBS1002424	Temperature	25°C
Item	Line Regulation 静的入力変動	Testing Circuitry	Figure A
Object	+24V4.2A		

1. Graph



Note: Slanted line shows the range of the rated input voltage.

(注) 斜線は定格入力電圧範囲を示す。

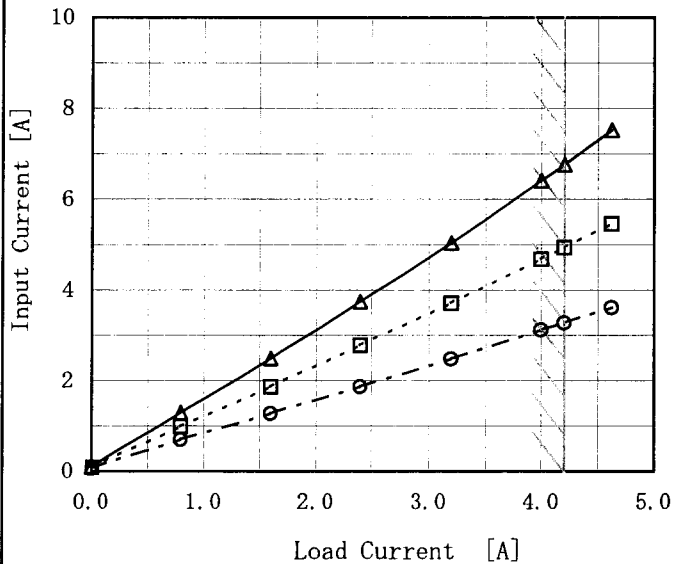
2. Values

Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
16	23.892	23.893
18	23.893	23.894
20	23.893	23.893
24	23.893	23.893
30	23.893	23.893
36	23.893	23.893
40	23.893	23.893
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Model		CBS1002424		Temperature		25°C																																																																								
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<p>Legend: —△— Load 100% - - □ - - Load 50% - - ○ - - Load 0%</p>				<table border="1"> <thead> <tr> <th rowspan="2">Input Voltage [V]</th> <th colspan="3">Input Current [A]</th> </tr> <tr> <th>Load 0%</th> <th>Load 50%</th> <th>Load 100%</th> </tr> </thead> <tbody> <tr><td>0</td><td>0.000</td><td>0.000</td><td>0.000</td></tr> <tr><td>4.0</td><td>0.000</td><td>0.000</td><td>0.000</td></tr> <tr><td>8.0</td><td>0.000</td><td>0.000</td><td>0.000</td></tr> <tr><td>12.0</td><td>0.016</td><td>0.015</td><td>0.016</td></tr> <tr><td>15.6</td><td>0.127</td><td>3.700</td><td>7.510</td></tr> <tr><td>16.0</td><td>0.124</td><td>3.614</td><td>7.310</td></tr> <tr><td>18.0</td><td>0.104</td><td>3.188</td><td>6.450</td></tr> <tr><td>20.0</td><td>0.096</td><td>2.857</td><td>5.800</td></tr> <tr><td>24.0</td><td>0.083</td><td>2.398</td><td>4.850</td></tr> <tr><td>28.0</td><td>0.076</td><td>2.068</td><td>4.145</td></tr> <tr><td>32.0</td><td>0.071</td><td>1.823</td><td>3.638</td></tr> <tr><td>36.0</td><td>0.067</td><td>1.632</td><td>3.260</td></tr> <tr><td>40.0</td><td>0.063</td><td>1.483</td><td>2.952</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> <tr><td>--</td><td>--</td><td>--</td><td>--</td></tr> </tbody> </table>				Input Voltage [V]	Input Current [A]			Load 0%	Load 50%	Load 100%	0	0.000	0.000	0.000	4.0	0.000	0.000	0.000	8.0	0.000	0.000	0.000	12.0	0.016	0.015	0.016	15.6	0.127	3.700	7.510	16.0	0.124	3.614	7.310	18.0	0.104	3.188	6.450	20.0	0.096	2.857	5.800	24.0	0.083	2.398	4.850	28.0	0.076	2.068	4.145	32.0	0.071	1.823	3.638	36.0	0.067	1.632	3.260	40.0	0.063	1.483	2.952	--	--	--	--	--	--	--	--	--	--	--	--
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Model	CBS1002424	Temperature	25°C
Item	Input Current (by Load Current) 入力電流 (負荷特性)	Testing Circuitry	Figure A
Object	_____		

1. Graph
- △— Input Volt. 18V
 - - □ - - Input Volt. 24V
 - - ○ - - Input Volt. 36V



Note: Slanted line shows the range of the rated load current.

(注) 斜線は定格負荷電流範囲を示す。

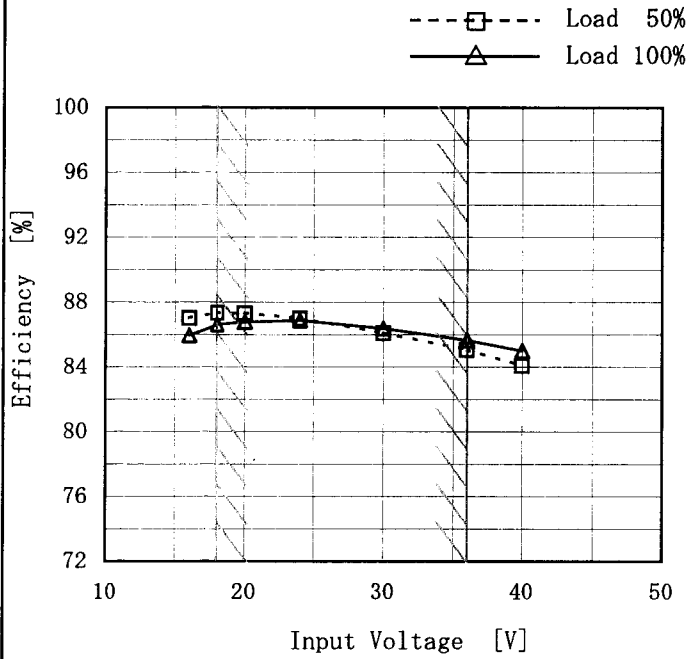
2. Values

Load Current [A]	Input Current [A]		
	Input Volt. 18[V]	Input Volt. 24[V]	Input Volt. 36[V]
0.00	0.102	0.081	0.066
0.80	1.295	0.984	0.695
1.60	2.493	1.862	1.275
2.40	3.744	2.778	1.866
3.20	5.040	3.710	2.480
4.00	6.410	4.680	3.116
4.20	6.760	4.930	3.274
4.62	7.520	5.460	3.612
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Model		CBS1002424		Temperature		25°C																																																				
Item		Input Power (by Load Current) 入力電力 (負荷特性)		Testing Circuitry		Figure A																																																				
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Model	CBS1002424	Temperature	25°C
Item	Efficiency (by Input Voltage) 効率 (入力電圧特性)	Testing Circuitry	Figure A
Object	_____		

1. Graph



Note: Slanted line shows the range of the rated input voltage.

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2. Values

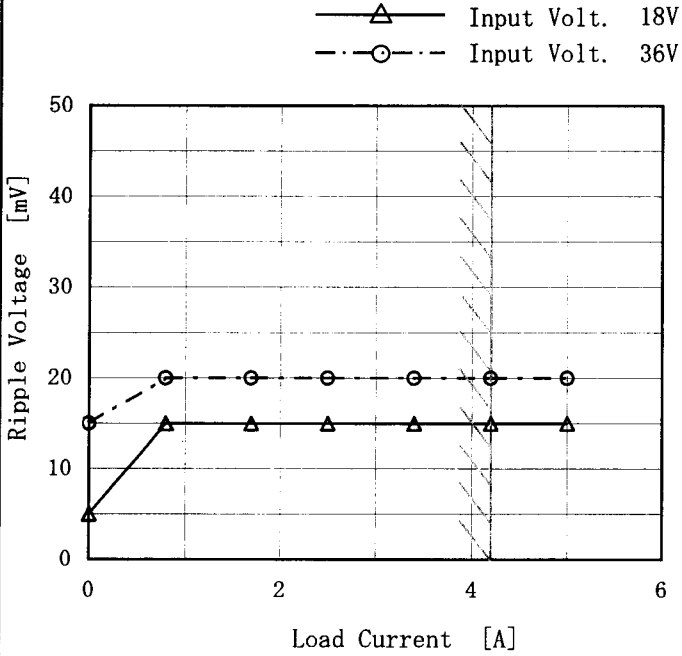
Input Voltage [V]	Efficiency [%]	
	Load 50%	Load 100%
16	87.0	85.9
18	87.3	86.6
20	87.3	86.8
24	87.0	86.9
30	86.1	86.4
36	85.1	85.7
40	84.1	85.0
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Model	CBS1002424	Temperature	25°C
Item	Ripple Voltage (by Load Current) リップル電圧 (負荷特性)	Testing Circuitry	Figure A
Object	+24V4.2A		

1. Graph



2. Values

Load Current [A]	Ripple Voltage [mV]	
	Input Volt. 18 [V]	Input Volt. 36 [V]
0.0	5	15
0.8	15	20
1.7	15	20
2.5	15	20
3.4	15	20
4.2	15	20
5.0	15	20
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Ripple Voltage is shown as p-p in the figure below.

Note: Slanted line shows the range of the rated load current.

リップル電圧は、下図 p-p 値で示される。
 (注) 斜線は定格負荷電流範囲を示す。

Ripple [mVp-p]

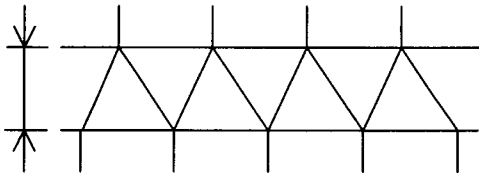
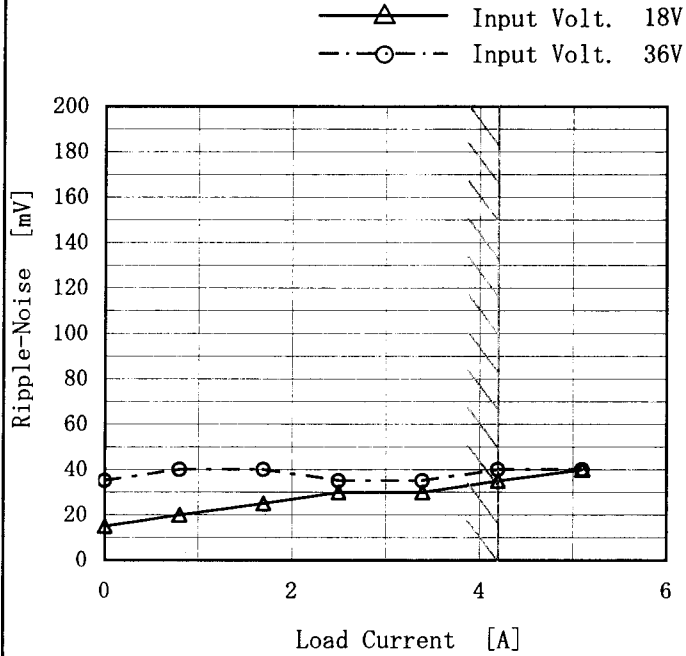


Fig. Complex Ripple Wave Form

図 リップル波形詳細図

Model	CBS1002424	Temperature	25°C
Item	Ripple-Noise リップルノイズ	Testing Circuitry	Figure A
Object	+24V4.2A		

1. Graph



2. Values

Load Current [A]	Ripple-Noise [mV]	
	Input Volt. 18 [V]	Input Volt. 36 [V]
0.0	15	35
0.8	20	40
1.7	25	40
2.5	30	35
3.4	30	35
4.2	35	40
5.1	40	40
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Ripple-Noise is shown as p-p in the figure below.
 Note: Slanted line shows the range of the rated load current.

リップルノイズは、下図 p-p 値で示される。
 (注) 斜線は定格負荷電流範囲を示す。

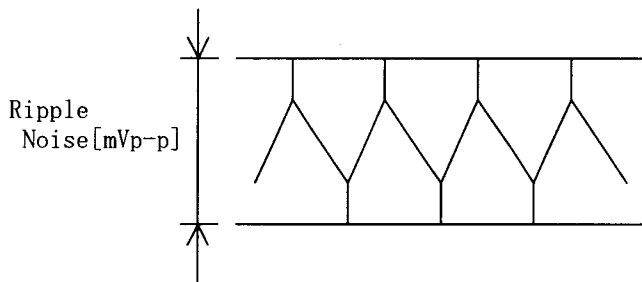


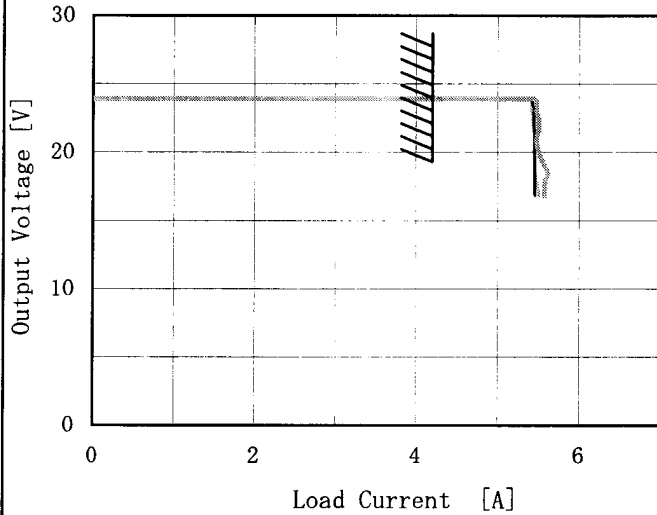
Fig. Complex Ripple Noise Wave Form
 図 リップルノイズ波形

Model	CBS1002424
Item	Overcurrent Protection 過電流保護
Object	+24V4.2A

Temperature 25°C
Testing Circuitry Figure A

1. Graph

————— Input Volt. 18V
 - - - - - Input Volt. 24V
 Input Volt. 36V



Note: Slanted line shows the range of the rated load current.

(注) 斜線は定格負荷電流範囲を示す。

Intermittent operation occurs when the output voltage is from 16.8V to 0V.

16.8V~0V間は、間欠モードとなる。

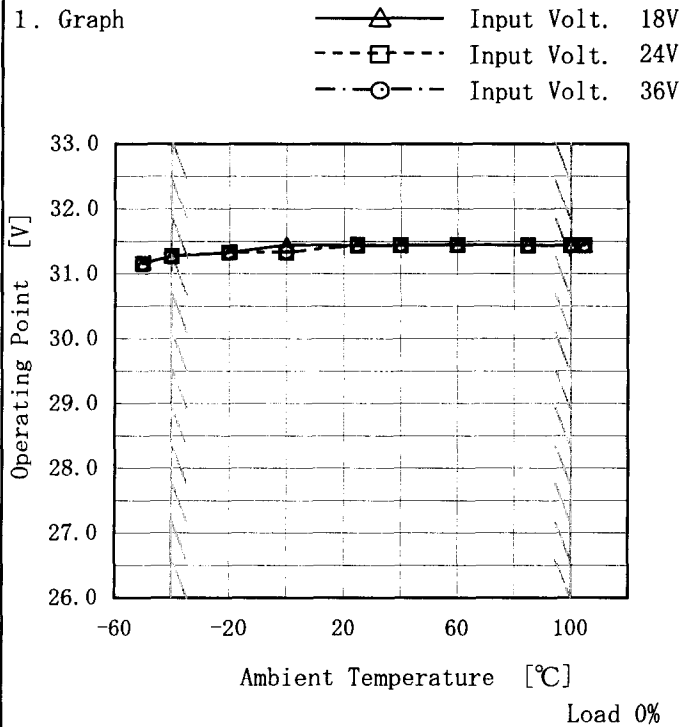
2. Values

Output Voltage [V]	Load Current [A]		
	Input Volt. 18[V]	Input Volt. 24[V]	Input Volt. 36[V]
24.0	4.28	4.28	4.28
22.8	5.45	5.42	5.49
21.6	5.45	5.45	5.50
19.2	5.46	5.46	5.56
16.8	5.46	5.51	5.57
--	--	--	--
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Model	CBS1002424
Item	Overtoltage Protection 過電圧保護
Object	+24V4.2A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Operating Point [V]		
	Input Volt. 18[V]	Input Volt. 24[V]	Input Volt. 36[V]
-50	31.16	31.16	31.16
-40	31.27	31.27	31.27
-20	31.33	31.33	31.33
0	31.45	31.33	31.33
25	31.44	31.45	31.44
40	31.44	31.44	31.44
60	31.45	31.45	31.45
85	31.44	31.44	31.44
100	31.44	31.44	31.44
105	31.44	31.44	31.44
--	--	--	--

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

Model	CBS1002424	Temperature	25°C
Item	Dynamic Load Response 動的負荷変動	Testing Circuitry	Figure A
Object	+24V4.2A		

Input Volt. 24 V
Cycle 1000 ms

Load Current



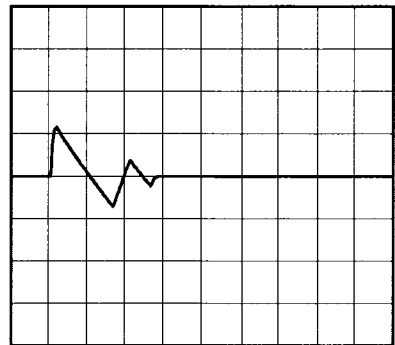
Min. Load (0A) ←→

Load 100% (4.2A)

500 mV/div



500 μs/div

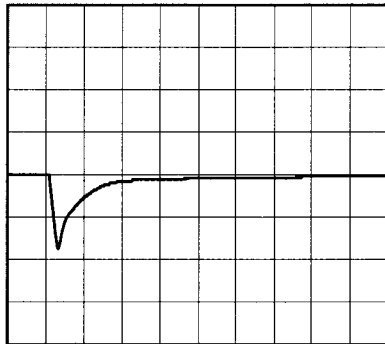


5 ms/div

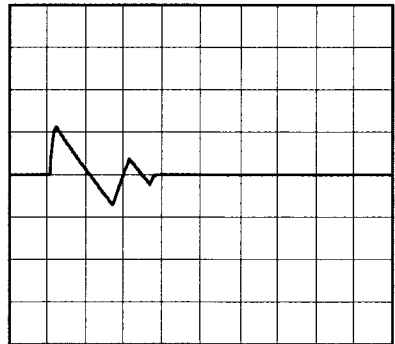
Min. Load (0A) ←→

Load 50% (2.1A)

500 mV/div



500 μs/div

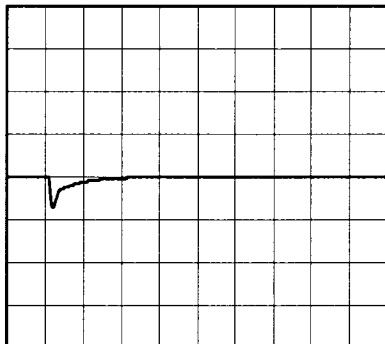


5 ms/div

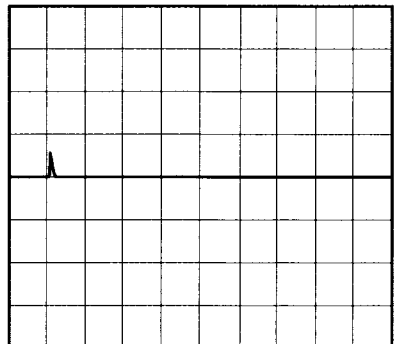
Load 10% (0.42A) ←→

Load 100% (4.2A)

500 mV/div



500 μs/div

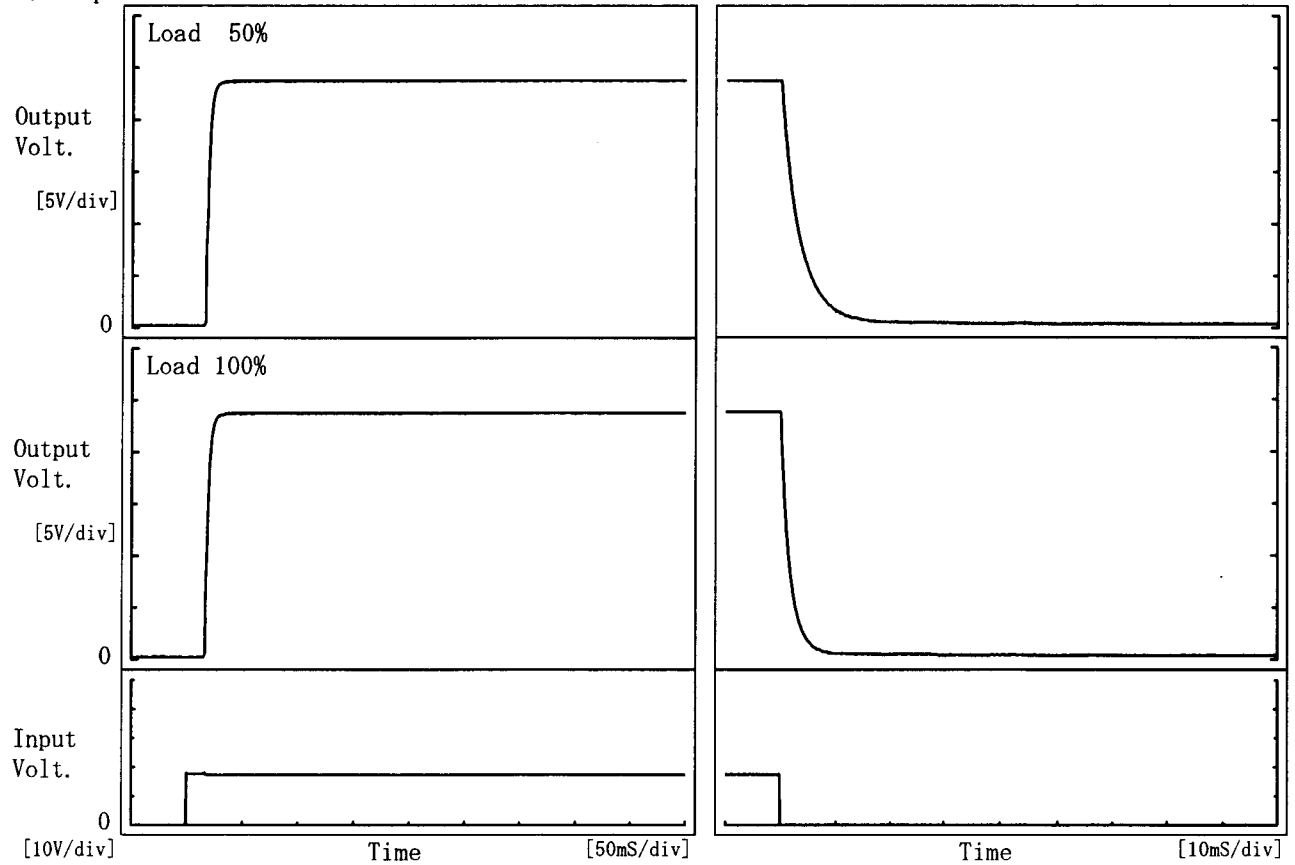


5 ms/div

Model	CBS1002424	Temperature	25°C
Item	Rise and Fall Time 立上り、立下り時間	Testing Circuitry	Figure A
Object	+24V4.2A		

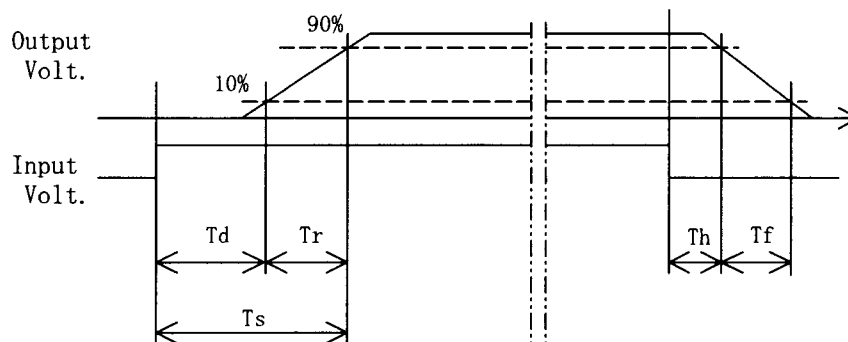
1. Graph

Input Volt. 18 V



2. Values

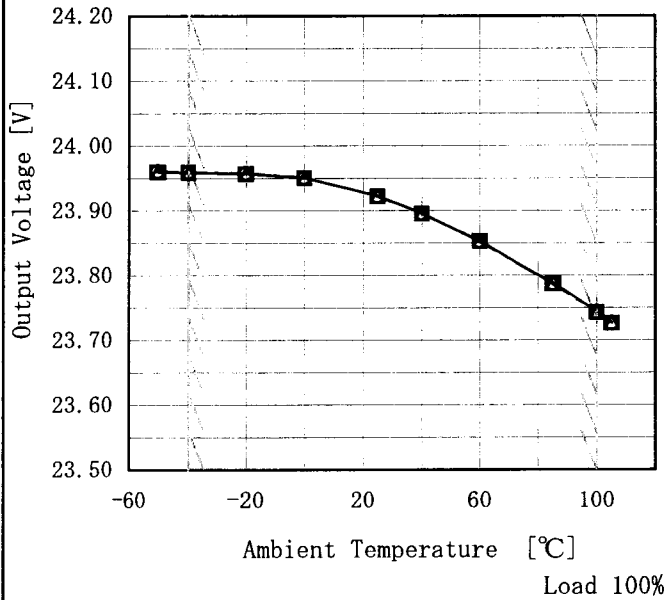
		[mS]				
Load \ Time	T d	T r	T s	T h	T f	
50 %	16.5	6.3	22.8	0.4	7.5	
100 %	16.5	6.5	23.0	0.3	3.8	



Model	CBS1002424
Item	Ambient Temperature Drift 周囲温度変動
Object	+24V4.2A

Testing Circuitry Figure A

1. Graph
- △— Input Volt. 18V
 - Input Volt. 24V
 - Input Volt. 36V



Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

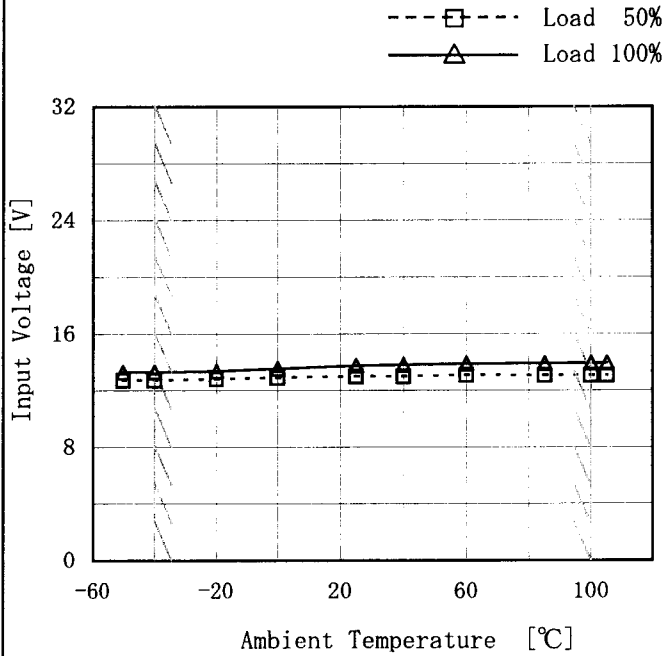
2. Values

Ambient Temperature [°C]	Output Voltage [V]		
	Input Volt. 18[V]	Input Volt. 24[V]	Input Volt. 36[V]
-50	23.960	23.960	23.961
-40	23.959	23.959	23.959
-20	23.957	23.957	23.958
0	23.951	23.951	23.951
25	23.923	23.922	23.922
40	23.896	23.896	23.895
60	23.853	23.853	23.853
85	23.788	23.787	23.787
100	23.744	23.743	23.742
105	23.727	23.726	23.726
--	—	—	—

Model	CBS1002424
Item	Minimum Input Voltage for Regulated Output Voltage 最低レギュレーション電圧
Object	+24V4.2A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Input Voltage [V]	
	Load 50%	Load 100%
-50	12.8	13.3
-40	12.8	13.4
-20	12.9	13.4
0	12.9	13.6
25	13.0	13.8
40	13.0	13.9
60	13.1	13.9
85	13.1	14.0
100	13.1	14.0
105	13.1	14.0
--	--	--

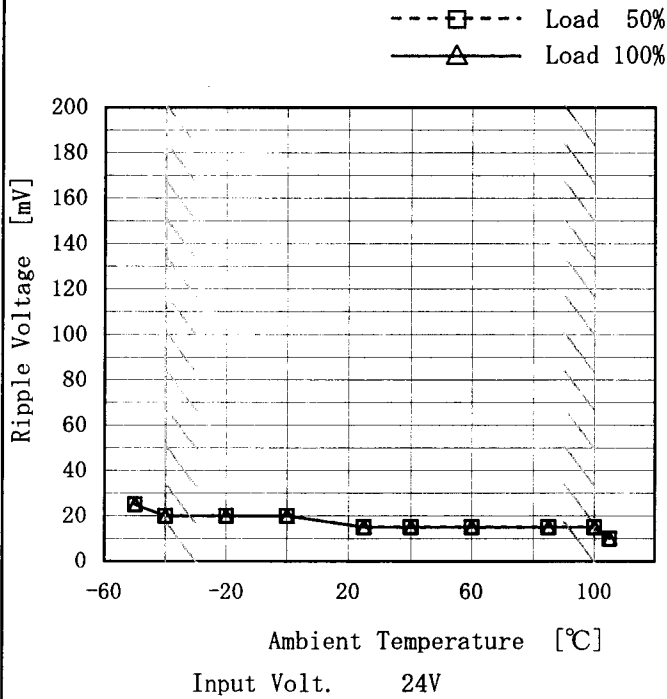
Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

Model	CBS1002424
Item	Ripple Voltage (by Ambient Temp.) リップル電圧 (周囲温度特性)
Object	+24V4.2A

Testing Circuitry Figure A

1. Graph



2. Values

Ambient Temperature [°C]	Ripple Voltage [mV]	
	Load 50%	Load 100%
-50	25	25
-40	20	20
-20	20	20
0	20	20
25	15	15
40	15	15
60	15	15
85	15	15
100	15	15
105	10	10
—	—	—

Note: Slanted line shows the range of the rated ambient temperature.

(注) 斜線は定格周囲温度範囲を示す。

Model	CBS1002424	Temperature	25°C																						
Item	Time Lapse Drift 経時ドリフト	Testing Circuitry	Figure A																						
Object	+24V4.2A																								
1. Graph		2. Values																							
<p>Output Voltage [V]</p> <p>Time [H]</p> <p>Input Volt. 24V Load 100%</p>		<table border="1"> <thead> <tr> <th>Time since start [H]</th> <th>Output Voltage [V]</th> </tr> </thead> <tbody> <tr><td>0.0</td><td>23.911</td></tr> <tr><td>0.5</td><td>23.894</td></tr> <tr><td>1.0</td><td>23.895</td></tr> <tr><td>2.0</td><td>23.895</td></tr> <tr><td>3.0</td><td>23.895</td></tr> <tr><td>4.0</td><td>23.895</td></tr> <tr><td>5.0</td><td>23.895</td></tr> <tr><td>6.0</td><td>23.896</td></tr> <tr><td>7.0</td><td>23.895</td></tr> <tr><td>8.0</td><td>23.895</td></tr> </tbody> </table>		Time since start [H]	Output Voltage [V]	0.0	23.911	0.5	23.894	1.0	23.895	2.0	23.895	3.0	23.895	4.0	23.895	5.0	23.895	6.0	23.896	7.0	23.895	8.0	23.895
Time since start [H]	Output Voltage [V]																								
0.0	23.911																								
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5.0	23.895																								
6.0	23.896																								
7.0	23.895																								
8.0	23.895																								

		Testing Circuitry Figure A
Model	CBS1002424	
Item	Output Voltage Accuracy 定電圧精度	
Object	+24V4.2A	

1. Output Voltage Accuracy

This is defined as the value of the output voltage, regulation load, ambient temperature and input voltage varied at random in the range as specified below.

Temperature : -40 ~ 100°C

Input Voltage : 18 ~ 36V

Load Current : 0 ~ 4.2A

* Output Voltage Accuracy = $\pm(\text{Maximum of Output Voltage} - \text{Minimum of Output Voltage}) / 2$

* Output Voltage Accuracy (Ration) = $\frac{\text{Output Voltage}}{\text{Rated Output Voltage}} \times 100$

1. 定電圧精度

周囲温度、入力電圧、負荷電流を下記仕様内で、任意に変動させたときの出力電圧の変動をいう。

周囲温度 : -40 ~ 100°C

入力電圧 : 18 ~ 36V

負荷電流 : 0 ~ 4.2A

* 定電圧精度(変動値) = $\pm(\text{出力電圧の最高値} - \text{出力電圧の最低値}) / 2$

* 定電圧精度(変動率) = $\frac{\text{変動値}}{\text{定格出力電圧}} \times 100$

2. Values

Item	Temperature [°C]	Input Voltage[V]	Output		Output Voltage Accuracy	
			Current[A]	Voltage[V]	Value [mV]	Ration [%]
Maximum Voltage	-40	36	4.2	23.959	±111	±0.5
Minimum Voltage	100	36	4.2	23.737		

		Testing Circuitry Figure A
Model	CBS1002424	
Item	Condense 結露特性	
Object	+24V4.2A	

1. Condensation test

Testing procedure is as follows.

- ① Keeping and cooling the unit in a tank at -10°C for an hour with the input off.
- ② Taking it out of the tank and dewing itself in a room where the temperature is 25°C and the humidity is 40%RH.
- ③ Testing electrical characteristics of the unit to confirm there be no fault.

1. 結露特性試験

入力を切った状態で、恒温槽で -10°C に冷却しておき、約1時間後に恒温槽から取り出し、室温 25°C 、湿度40%RHの状態におき結露させ、その電気的特性の測定を行い異常のないことを確認する。

2. Values

Item	Data	Testing Conditions
Output Voltage [V]	23.923	Input Volt. : 24V, Load Current. : 4.2A
Line Regulation [mV]	3	Input Volt. : 18~36V, Load Current. : 4.2A
Load Regulation [mV]	1	Input Volt. : 24V, Load Current. : 0~4.2A

Model		CBS1002424	Temperature		25°C
Item		Line Noise Tolerance 入力雑音耐量	Testing Circuitry		Figure B
Object		+24V4.2A			

1. Conditions

- Input Voltage : 24 V
- Pulse Voltage : 2000 V
- Pulse Cycle : 16.7 mS
- Pulse Input Duration : 1 min. or more
- Load : 100 %

2. Results

Pulse Width [nS]	MODE		No protection failure should occur	DC-like Regulation of Output Voltage
		POLARITY	保護回路の誤動作がない	出力電圧の直流的変動
50	COMMON	+	OK	no fluctuation
		-	OK	no fluctuation
	NORMAL	+	OK	no fluctuation
		-	OK	no fluctuation
1000	COMMON	+	OK	no fluctuation
		-	OK	no fluctuation
	NORMAL	+	OK	no fluctuation
		-	OK	no fluctuation

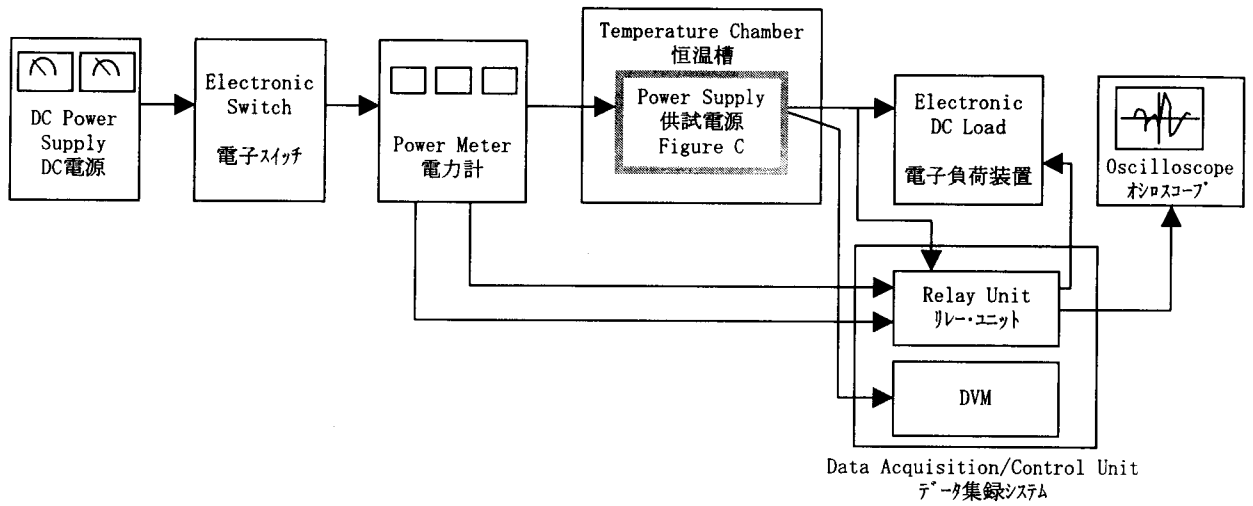


Figure A

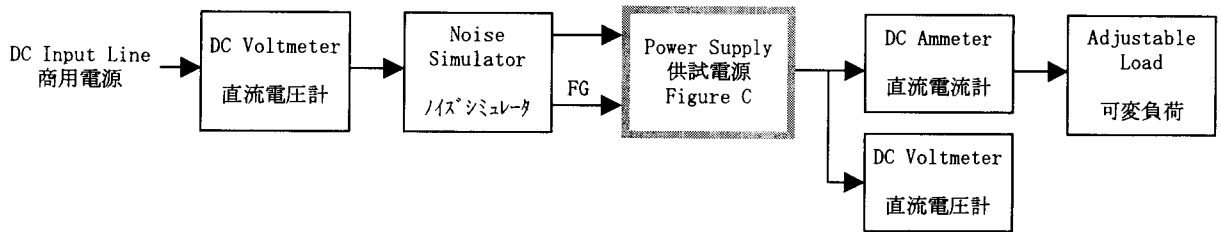
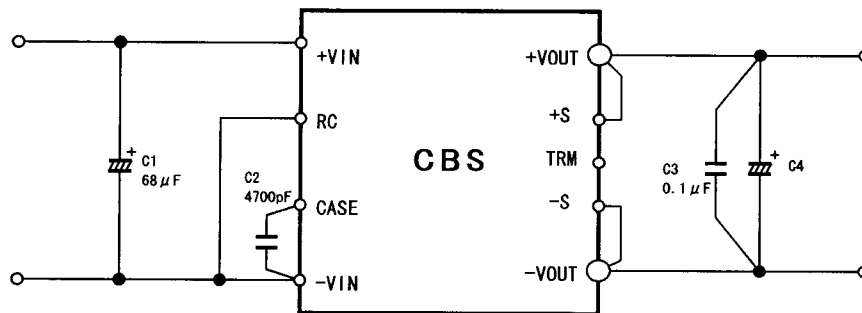


Figure B



C1 : 50V 68 μF
 C2 : 4700pF
 C3 : 50V 0.1 μF
 C4 : 35V 220 μF × 2 (-40°C ≤ T_B ≤ -20°C)
 35V 220 μF (-20°C < T_B ≤ 100°C)
 T_B : Base Plate Temp.

Figure C